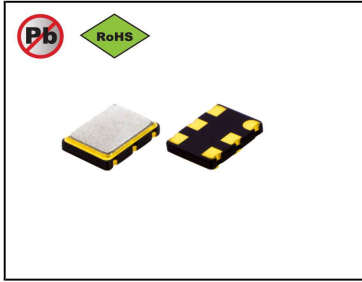


KPV7

SMD VCXO



■ LV-PECL VCXO 6PAD 7.0x5.0

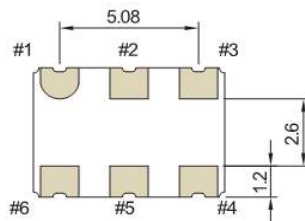
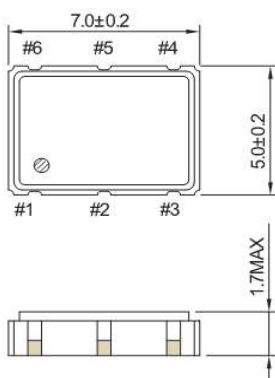
- Miniature Ceramic Surface Mount package
- Wide pulling range and Low phase noise
- Fundamental crystal design

- Applications : Telecommunications, Network equipment

■ Standard Specification

| | | |
|--------------------------------------|-------------------------------|--------------------|
| Frequency range | 10MHz to 250MHz | |
| Supply voltage | 3.3V | |
| Pin1 Control Voltage | 1.65V±1.35V | |
| Frequency Pull Range | ±50ppm Min or please specify | |
| Linearity | 10% Max | |
| Frequency slope | Positive | |
| Current consumption | 90mA Max or please specify | |
| Frequency Stability | ±25ppm, ±50ppm, ±100ppm | |
| Operating Temperature Range | -20 to 70°C, -40 to 85°C | |
| Storage Temperature Range | -55 to 125°C | |
| Output load condition | 50 Ω (connected to Vdd-2.0V) | |
| Symmetry (Duty) | 50% ±10% | |
| Output rise and fall time (Tr /Tf) | 1ns Max | |
| Output high level | 2.215V ~ 2.420V (3.3V Vdd) | |
| Output low level | 1.470V ~ 1.745V (3.3V Vdd) | |
| Pin 2 | Enable input voltage | 70% Vdd Min or N.C |
| Function | Disable input voltage | 30% Vdd Max |
| Oscillation start up time | 5ms Max | |
| Aging (Initial first year) | ± 3ppm Max | |
| Phase jitter (12KHz ~ 20MHz) | 89 fs typ at 122.88MHz (3.3V) | |

■ Dimensions (mm)



CONNECTION

- #1 V.C
- #2 Tri-State
- #3 GND
- #4 OUTPUT
- #5 COMP-OUTPUT
- #6 Vdd

• Recommended Soldering Pattern

