

Crystal Oscillator Road Map



SMD TCXO

Clipped Sine Wave

1.8V to 3.3V

0.5ppm Available

over -30 to 85°C

3225 12 to 52.000MHz

2520 12 to 52.000MHz

2016 12 to 52.000MHz

Production

Under Development

Planning

***Developed frequency list**

SIZE	Clipped Sine	SPEC
3225	12M	1ppm@-30/85°C
	12.8M	1ppm@-30/85°C
	24M	1ppm@-30/85°C
	26M	1ppm@-30/85°C
	32M	1ppm@-30/85°C
2520	12M	0.5ppm@-30/85°C
	19.2M	2ppm@-30/85°C
	24M	0.5ppm@-30/85°C
	26M	0.5ppm@-30/85°C
	32M	0.5ppm@-30/85°C
	40M	2ppm@-30/85°C
	48M	2ppm@-30/85°C
2016	32M	0.5ppm@-30/85°C
	48M	2ppm@-30/85°C

*** Manager Connection: 26M TCXO for GPS**



Crystal Oscillator Road Map

SMD CMOS OSC

Fundamental or 3rd O/T

Low Supply Voltage
Available 0.9 to 1.5V

Wide Temp Range
Available -55~125°C

Ultra low Jitter available
0.1ps RMS max

AEC-Q100 Factory Qualification

7050	0.75 to 220.000MHz, 32.768KHz
5032	0.75 to 220.000MHz, 32.768KHz
3225	0.75 to 180.000MHz, 32.768KHz
2520	0.75 to 125.000MHz, 32.768KHz
2016	12 to 62.500MHz

Production
Under Development
Planning

Phase Jitter(12k-20M)
37fs typ (f=156.25M)
66fs typ (f=54M)

1612 SAMPLE AVAILABLE

PROG CMOS OSC

Fundamental + PLL
Glacier IC design
2.5V or 3.3V

Jitter 0.6typ, 1.0max

7050	10 to 250.000MHz
5032	10 to 250.000MHz
3225	10 to 250.000MHz

PROG CMOS OSC

Fundamental + PLL
Cypress IC design
2.7 to 5.0V

7050	Vdd 5.0V : 133.000MHz Max
5032	Vdd 3.3V : 100.000MHz Max
	Vdd 2.7V : 66.600MHz Max





Crystal Oscillator Road Map

STD CMOS VCXO

Fundamental design
1.8V to 5.0V

Wide Temp Range
Available -55~125°C

AEC-Q100 Factory Qualification

7050	1 to 77.760MHz
5032	1 to 77.760MHz

3225	1 to 54.000MHz (±80ppm min Pulling)
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2520 4PAD

SAMPLE AVAILABLE

Production
Under Development
Planning

HFF CMOS VCXO

Mesa Fundamental design
3.3V

7050	80 to 170.000MHz Phase Jitter : 1.0ps max
5032	80 to 170.000MHz Phase Jitter : 1.0ps max

Phase Jitter (12k-20M)
75fs typ (f=122.88M)

PROG CMOS VCXO

Fundamental + PLL
Glacier IC design
2.5V or 3.3V

7050	10 to 250.000MHz
5032	10 to 250.000MHz
3225	10 to 250.000MHz

Jitter 0.6typ, 1.0max





Crystal Oscillator Road Map

PECL/LVDS OSC

Fundamental or 3rd O/T

1.8V, 2.5V, 3.3V

7050

12.5 to 312.500MHz

5032

12.5 to 312.500MHz

3225

12.5 to 220.000MHz

Ultra low Jitter available
0.1ps RMS max

Phase Jitter (12k-20M)

76fs typ (f=156.25M)

200fs typ (f=312.5M)

Production

Under
Development

Planning

HCSL OSC

Fundamental or 3rd O/T

2.5V, 3.3V

7050

20 to 170.000MHz

5032

20 to 170.000MHz

3225

20 to 170.000MHz

Phase Jitter (12k-20M)

175fs typ (f=100.M)

PROG PECL/LVDS OSC

Fundamental + PLL

Glacier IC design

2.5V or 3.3V

7050

10 to 800.000MHz

5032

10 to 800.000MHz

3225

10 to 800.000MHz

Jitter 0.6typ, 1.0max





Crystal Oscillator Road Map

STD PECL/LVDS
VCXO

7050

9.5 to 77.760MHz

Fundamental design
3.3V

5032

9.5 to 77.760MHz

Production

Under
Development

Planning

HFF PECL/LVDS
VCXO

7050

80 to 212.500MHz
Phase Jitter : 1.0ps max

Mesa Fundamental design
3.3V

5032

80 to 212.500MHz
Phase Jitter : 1.0ps max

Phase Jitter (12k-20M)
90fs typ (f=122.88M)
95fs typ (f=204.8M)

PROG PECL/LVDS
VCXO

7050

10 to 800.000MHz

Fundamental + PLL
Glacier IC design
2.5V or 3.3V

5032

10 to 800.000MHz

Jitter 0.6typ, 1.0max

3225

10 to 800.000MHz





Crystal Oscillator Road Map

Metal Dip OSC

Fundamental or 3rd O/T

3.3V to 5.0V

HALF

32.768K to 250.000MHz

FULL

32.768K to 250.000MHz

Wide Temp Range

Available -55~125°C

Production

Under
Development

Planning

Metal Dip VCXO

Fundamental or Multi

3.3V to 5.0V

HALF

1 to 200.000MHz

FULL

1 to 200.000MHz

Plastic J-lead OSC

Fundamental or 3rd O/T

2.5V to 5.0V

14.0 x 9.8

32.768K to 200.000MHz

